

[10191/1663]

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s)

THOMAS et al.

Confirmation No.:

8931

Serial No.

09/766,102

Filed

January 19, 2001

For

METHOD FOR PROTECTING A MICROCOMPUTER

SYSTEM AGAINST MANIPULATION OF DATA STORED

IN A STORAGE ARRANGEMENT OF THE

MICROCOMPUTER SYSTEM

Examiner

R. Baum

Art Unit

2136

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Customer No.:

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9/22/04

Alexandria, VA 22313-1450, on

Atty's Reg. # 41,172

Atty's Signature

DERVIS MAGISTRE KENYON & KENYON

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT TRANSMITTAL**

SIR:

Please find an Amendment transmitted herewith for filing in the above-identified patent application.

No fee is believed to be required. However, if any fee is required, please use Deposit Account No. 11-0600. A duplicate copy of this transmittal letter is enclosed for that purpose.

Respectfully submitted,

**KENYON & KENYON** 

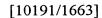
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#### **RESPONSE**

SIR:

In response to the Office Action dated August 4, 2004, reconsideration of the above-referenced application is respectfully requested for the reasons presented below.

## Remarks

Claims 1-16 remain pending in the above-referenced application.

Claims 1-16 stand rejected under 35 U.S.C. § 102(e) as being anticipated by United States Patent No. 6,138,236 to Mirov. This analysis focuses on the claimed feature according to which both the comparison code and the security code are generated from the same individual identifier. In order to anticipate a claim, a reference must identically teach every single limitation recited in the claim. Applicants submit that Mirov does not anticipate any of claims 1-16 because nowhere in Mirov is there taught two codes, one a security code and the other a comparison code, that are generated from the same individual identifier.

In the rejection, the Examiner cites the Background Of The Invention and the Summary Of the Invention in Mirov as support. The Background portion of Mirov describes a prior art system in which the micro-code for boot up is stored in either a ROM or a PROM.